

REMARKS

Claims 1-57 and 73-87 are pending in the present application. Claims 1-72 were presented for examination. Claims 58-72 have been cancelled and claims 73-87 added by amendment.

In the office action mailed July 24, 2003 ("the Office Action"), the Examiner objected to the amendments to claims 63 and 68 made in the previously filed amendment on March 26, 2003. The Examiner also indicated that the reissue declaration was defective. Claims 1-72 were rejected as being based upon a defective reissue declaration under 35 U.S.C. 251. Claims 58 and 61-72 were rejected under 35 U.S.C. 112, first paragraph. Claims 58 and 61-72 were further rejected under 35 U.S.C. 251 as not being directed to the same invention as that disclosed in the original patent. Claims 58, 63, and 64 were rejected under 35 U.S.C. 102(a) as being anticipated by the Applicants' admitted prior art ("the AAPA"). Claims 61, 62, 67, 68, 71, and 72 were rejected under 35 U.S.C. 103(a) as being unpatentable over the AAPA.

With respect to the objections to the amendments made to claims 63 and 68, the issue is moot in light of the cancellation of claims 63 and 68 by amendment.

With respect to the defective declaration and the rejection of claims 1-72 based upon a defective declaration, Applicants will submit a declaration in compliance with 37 C.F.R. 1.175 at a later time when claims are in condition for allowance.

With respect to the rejection of claims 58 and 61-72 under 35 U.S.C. 112, first paragraph; the rejection of claims 58-72 under 35 U.S.C. 112, first paragraph; the rejection of claims 58 and 61-72 under 35 U.S.C. 251 as not being directed to a common invention as that disclosed in the original patent; the rejection of claims 58, 63, and 64 under 35 U.S.C. 102(a) as being anticipated by the AAPA; and the rejection of claims 61, 62, 67, 68, 71, and 72 were rejected under 35 U.S.C. 103(a) as being unpatentable over the AAPA, are moot in view of the previously mentioned cancellation of claims 58-72 by amendment.

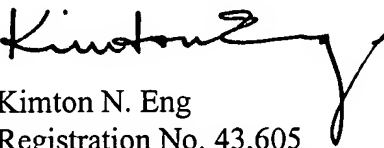
With respect to the rejection of claims 53-57 under 35 U.S.C. 112, first paragraph, the Examiner is directed to Figures 3 and 4, and the material found at col. 4, line 33-col. 5, line 17, and at col. 5, line 37-col. 6, line 21, of the original patent to find support for the claims. As described therein, a pulse signal is generated in response to the rising edge of a clock signal applied to the input of passgate 120. *See* Figure 3 and col. 5, lines 46-49. Being enabled, the passgate 120 provides the clock signal to its output node until the clock signal propagates

through a NAND gate 150, at which time the output signal of the NAND gate 150 deactivates the passgate 120. *See* Figures 3 and 4, and col. 5, lines 52-61. Also in response to the output signal of the NAND gate, an NMOS transistor 132 is switched ON to couple the output of the passgate 120 to ground. *See* Figures 3 and 4, and col. 5, line 66-col. 6, line 3. The subject matter of claim 54 is described at least at col. 5, lines 52-61. The subject matter of claim 55 is described at least at col. 5, line 57-col. 6, line 3. The subject matter of claim 56 is described at least at col. 5, lines 5-17. The subject matter of claim 57 is found at least in Figure 4 and described in the related description. Consequently, claims 53-57 are fully supported by the specification of the original patent. Therefore, the rejection of claims 53-57 under 35 U.S.C. 112, first paragraph, should be withdrawn.

As previously mentioned, claims 73-87 have been added by amendment. As with claims 53-57, support for new claims 73-87 can be found at least in Figures 3 and 4, and at col. 4, line 33-col. 5, line 17, and at col. 5, line 37-col. 6, line 21, of the original patent.

All of the claims pending in the present application are in condition for allowance. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,
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